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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"6108423".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 15:49
L2	1452	380/200-203.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 15:52
L3	1284	726/26,32,33.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 15:52
L4	116	l3 and @ad<"19981001"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 15:53
L5	0	l4 and (stop$4 or prohibit$4 or inhibit$4) with digital with concurrent$4 with output$4 with analog$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 15:54
L6	0	l4 and (stop$4 or prohibit$4 or inhibit$4) with digital with output$4 with analog$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 15:55
L7	2485	726/26-33.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 15:55

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L8	283	l7 and @ad<"19981001"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 15:57
L9	0	l8 and (stop$4 or inhibit$4 or prohibit$4) near10 output$4 near10 digital with (output$4 near4 analog)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 16:00
L10	76	(output$4 near3 first adj signal) with (stop$4 or prohibit$4 or inhibit$4) near10 (second adj signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 16:01
L11	39	l10 and @ad<"19981001"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 16:08
L12	31	(stop$4 or prohibit$4 or inhibit$4) with (concurrent$3 or simultan$5) with (first adj signal and second adj signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 16:10
L13	16	l12 and @ad<"19981001"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 16:11

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L16	0	l15 and (stop$4 or inhibit$4 or prohibit$4) adj (first adj signal or digital adj signal).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 16:15
L17	26	l15 and (stop$4 or inhibit$4 or prohibit$4) near10 (first adj signal or digital adj signal).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 16:15
L18	20	l17 and @ad<"19981001"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 16:26
L19	255377	sako.in. or inokuchi.in. or obata.in. or ito.in. or sakurai.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 16:26
L20	0	l19 and l12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 16:27

EAST Search History

L21	1	l19 and l17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/10 16:27

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Computer Graphics staff
August 1979 **ACM SIGGRAPH Computer Graphics**, Volume 13 Issue 3
Publisher: ACM Press
Full text available: pdf(15.01 MB) Additional Information: full citation, references, citings

2 Level set and PDE methods for computer graphics

David Breen, Ron Fedkiw, Ken Museth, Stanley Osher, Guillermo Sapiro, Ross Whitaker
August 2004 **ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04**
Publisher: ACM Press
Full text available: pdf(17.07 MB) Additional Information: full citation, abstract, citings

Level set methods, an important class of partial differential equation (PDE) methods, define dyr level set (iso-surface) of a sampled, evolving nD function. The course begins with preparatory r concept of using partial differential equations to solve problems in computer graphics, geometri This will include the structure and behavior of several different types of differential equations, e

3 Special section: Reasoning about structure, behavior and function

B. Chandrasekaran, Rob Milne
July 1985 **ACM SIGART Bulletin**, Issue 93
Publisher: ACM Press
Full text available: pdf(5.13 MB) Additional Information: full citation, abstract, references, citings

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4 Special issue: AI in engineering

D. Sriram, R. Joobbani
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from over six countries. About half the papers were received over the computer network.

5 Real-time shading

Marc Olano, Kurt Akeley, John C. Hart, Wolfgang Heidrich, Michael McCool, Jason L. Mitchell, Randi
August 2004 **ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04**

Publisher: ACM Press

Full text available: pdf(7.39 MB) Additional Information: full citation, abstract

Real-time procedural shading was once seen as a distant dream. When the first version of this c real-time shading was possible, but only with one-of-a-kind hardware or by combining the effec rendering passes. Today, almost every new computer comes with graphics hardware capable of thousands to tens of thousands of instructions. This course has been redesigned to address tod

6 Seeing, hearing, and touching: putting it all together

Brian Fisher, Sidney Fels, Karon MacLean, Tamara Munzner, Ronald Rensink
August 2004 **ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04**

Publisher: ACM Press

Full text available: pdf(20.64 MB) Additional Information: full citation

7 Status report of the graphic standards planning committee of ACM/SIGGRAPH: State-of-tl packages

Compuater Graphics staff
September 1977 **ACM SIGGRAPH Computer Graphics**, Volume 11 Issue 3

Publisher: ACM Press

Full text available: pdf(9.03 MB) Additional Information: full citation, references

8 The elements of nature: interactive and realistic techniques

Oliver Deusen, David S. Ebert, Ron Fedkiw, F. Kenton Musgrave, Przemyslaw Prusinkiewicz, Doug
August 2004 **ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04**

Publisher: ACM Press

Full text available: pdf(17.65 MB) Additional Information: full citation, abstract

This updated course on simulating natural phenomena will cover the latest research and produc most of the elements of nature. The presenters will provide movie production, interactive simul on the difficult task of photorealistic modeling, rendering, and animation of natural phenomena. of the latest interactive graphics hardware-based simulation techniques and the latest physics-t

9 Data Security

Dorothy E. Denning, Peter J. Denning
September 1979 **ACM Computing Surveys (CSUR)**, Volume 11 Issue 3

Publisher: ACM Press

Full text available: pdf(1.97 MB) Additional Information: full citation, references, citings, index te

10 Software safety: why, what, and how

Nancy G. Leveson
June 1986 **ACM Computing Surveys (CSUR)**, Volume 18 Issue 2

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Full text available: pdf(4.18 MB) Additional Information: full citation, abstract, references, citings

Software safety issues become important when computers are used to control real-time, safety attempts to explain why there is a problem, what the problem is, and what is known about how relatively new software research area, emphasis is placed on delineating the outstanding issues

11 Data base directions: the next steps

John L. Berg
November 1976 **ACM SIGMOD Record , ACM SIGMIS Database**, Volume 8 , 8 Issue 4 , 2
Publisher: ACM Press
Full text available: pdf(9.95 MB) Additional Information: full citation, abstract, citings

What information about data base technology does a manager need to make prudent decisions To provide this information the National Bureau of Standards and the Association for Computinç workshop of approximately 80 experts in five major subject areas. The five subject areas were · government regulations, standards, and user experience. Each area prepared a report containe proceedings p ...

Keywords: DBMS, auditing, cost/benefit analysis, data base, data base management, governm objectives, privacy, security, standards, technology assessment, user experience

12 Pipeline Architecture

C. V. Ramamoorthy, H. F. Li
March 1977 **ACM Computing Surveys (CSUR)**, Volume 9 Issue 1
Publisher: ACM Press
Full text available: pdf(3.53 MB) Additional Information: full citation, references, citings, index terms

13 Eliminating stack overflow by abstract interpretation

John Regehr, Alastair Reid, Kirk Webb
November 2005 **ACM Transactions on Embedded Computing Systems (TECS)**, Volume 4 Issue 4
Publisher: ACM Press
Full text available: pdf(510.78 KB) Additional Information: full citation, abstract, references, index t

An important correctness criterion for software running on embedded microcontrollers is *stack s* stack does not overflow. Our first contribution is a method for statically guaranteeing stack safe software using an approach based on context-sensitive dataflow analysis of object code. We ha analysis tool that targets software for Atmel AVR microcontrollers and tested it on embedded ap

Keywords: Microcontroller, abstract interpretation, call stack, context sensitive, dataflow analy network

14 A Structural View of PL/I

David Beech
March 1970 **ACM Computing Surveys (CSUR)**, Volume 2 Issue 1
Publisher: ACM Press
Full text available: pdf(2.86 MB) Additional Information: full citation, references, citings, index terms

15 Document Formatting Systems: Survey, Concepts, and Issues

Richard Furuta, Jeffrey Scofield, Alan Shaw
September 1982 **ACM Computing Surveys (CSUR)**, Volume 14 Issue 3
Publisher: ACM Press

Full text available: pdf(5.36 MB) Additional Information: full citation, references, citings, index te

16 Speech synthesis for computer assisted instruction: The MISS system and its applications

William R. Sanders, Gerard V. Benbassat, Robert L. Smith

February 1976 **ACM SIGCUE Outlook , ACM SIGCSE Bulletin , Proceedings of the ACM SIGCS symposium on Computer science and education**, Volume 10 , 8 Issue SI , 1

Publisher: ACM Press

Full text available: pdf(1.03 MB) Additional Information: full citation, abstract, references, citings

The Institute for Mathematical Studies in the Social Sciences at Stanford (IMSSS) has develope (Microprogrammed Intoned Speech Synthesizer), designed to test the effectiveness of compute of complex CAI programs. No one method of computer controlled speech production is complet computer-assisted instruction (CAI). The choice of synthesis method is strongly related to the k

17 Survey of software tools for evaluating reliability, availability, and serviceability

Allen M. Johnson, Miroslaw Malek

September 1988 **ACM Computing Surveys (CSUR)**, Volume 20 Issue 4

Publisher: ACM Press

Full text available: pdf(3.79 MB) Additional Information: full citation, abstract, references, citings

In computer design, it is essential to know the effectiveness of different design options in imprc dependability. Various software tools have been created to evaluate these parameters, applying techniques, and this paper reviews those related primarily to reliability, availability, and service models used, type of systems modeled, inputs, and outputs are given for each package. Examp

18 Reflections on building two Go programs

Bruce Wilcox

October 1985 **ACM SIGART Bulletin**, Issue 94

Publisher: ACM Press

Full text available: pdf(1.42 MB) Additional Information: full citation, abstract, references, citings

From 1972 to 1979 I co-designed and built what became the world's strongest computer Go prc Program [1]. It took 7 person-years, 8K lines of LISP, 3 megabytes of memory, and an IBM ma similar program, called NEMESIS...the Go Master (tm). It has taken 1 person-year, 13.5K lines and an IBM-PC. They play at a similar strength and by similar means. This article discusses bot the ...

19 Virtual time

David R. Jefferson

July 1985 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volun

Publisher: ACM Press

Full text available: pdf(1.82 MB) Additional Information: full citation, abstract, references, citings

Virtual time is a new paradigm for organizing and synchronizing distributed systems which can distributed discrete event simulation and distributed database concurrency control. Virtual time real time in much the same way that virtual memory provides an abstraction of real memory. It Warp mechanism, a synchronization protocol distinguished by its reliance on lookahead-rollback

20 A comparison of the object-oriented features of Ada 95 and Java

Benjamin M. Brosgol

November 1997 **Proceedings of the conference on TRI-Ada '97 TRI-Ada '97**

Publisher: ACM Press

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5 Software safety: why, what, and how

Nancy G. Leveson
June 1986 **ACM Computing Surveys (CSUR)**, Volume 18 Issue 2
Publisher: ACM Press

Full text available: pdf(4.18 MB) Additional Information: full citation, abstract, references, citings, index terms, review

Software safety issues become important when computers are used to control real-time, safety-critical processes. This survey attempts to explain why there is a problem, what the problem is, and what is known about how to solve it. Since this is a relatively new software research area, emphasis is placed on delineating the outstanding issues and research topics.

6 Pipeline Architecture

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March 1977 **ACM Computing Surveys (CSUR)**, Volume 9 Issue 1
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David Beech
March 1970 **ACM Computing Surveys (CSUR)**, Volume 2 Issue 1
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Dorothy E. Denning, Peter J. Denning
September 1979 **ACM Computing Surveys (CSUR)**, Volume 11 Issue 3
Publisher: ACM Press
Full text available: pdf(1.97 MB) Additional Information: full citation, references, citings, index terms

9 A scheduling philosophy for multiprocessing systems

Butler W. Lampson
May 1968 **Communications of the ACM**, Volume 11 Issue 5
Publisher: ACM Press
Full text available: pdf(2.11 MB) Additional Information: full citation, references, citings

Keywords: interlocks, interrupt systems, multiprocessing, priority, process, protection, scheduling, time-sharing

10 High level hierarchical fault simulation techniques

William A. Rogers, Jacob A. Abraham

March 1985 **Proceedings of the 1985 ACM thirteenth annual conference on Computer Science CSC '85**
Publisher: ACM Press
Full text available: pdf(1.05 MB) Additional Information: full citation, abstract, references, citings, index terms

This paper presents techniques for simulating directly from a hierarchical circuit description without flattening to the level of primitives. An overview of traditional fault simulation techniques is followed by details of the hierarchical techniques. The fault model is shown to be decoupled from the simulator programs through the use of a fault library. The fault library allows the user to mix both functional and technology-dependent fault models, which allows fault simulation and conseque ...

11 ALGOL: ALGOL bulletin no. 27
F. G. Duncan
September 1968 **ACM SIGPLAN Notices**, Volume 3 Issue 9
Publisher: ACM Press
Full text available: pdf(3.04 MB) Additional Information: full citation

12 Semantic Road Maps for Literature Searchers
Lauren B. Doyle
October 1961 **Journal of the ACM (JACM)**, Volume 8 Issue 4
Publisher: ACM Press
Full text available: pdf(1.46 MB) Additional Information: full citation, references, citings, index terms

13 Operational characteristics of a harware-based pattern matcher
Roger L. Haskin, Lee A. Hollaar
March 1983 **ACM Transactions on Database Systems (TODS)**, Volume 8 Issue 1
Publisher: ACM Press
Full text available: pdf(1.84 MB) Additional Information: full citation, abstract, references, citings, index terms

The design and operation of a new class of hardware-based pattern matchers, such as would be used in a backended database processor in a full-text or other retrieval system, is presented. This recognizer is based on a unique implementation technique for finite state automata consisting of partitioning the state table among a number of simple digital machines. It avoids the problems generally associated with implementing finite state machines, such as large state table memories, complex cont ...

Keywords: backend processors, computer system architecture, finite state automata, full text retrieval systems, text searching

14 Reflections on building two Go programs
Bruce Wilcox
October 1985 **ACM SIGART Bulletin**, Issue 94
Publisher: ACM Press
Full text available: pdf(1.42 MB) Additional Information: full citation, abstract, references, citings

From 1972 to 1979 I co-designed and built what became the world's strongest computer Go program, the Reitman-Wilcox Go Program [1]. It took 7 person-years, 8K lines of LISP, 3 megabytes of memory, and an IBM mainframe. Recently I constructed a similar program, called NEMESIS...the Go Master (tm). It has taken 1 person-year, 13.5K lines of

C, 146 kilobytes of memory, and an IBM-PC. They play at a similar strength and by similar means. This article discusses both how I went about reengineering the ...

15 Implementation of precise interrupts in pipelined processors

James E. Smith, Andrew R. Pleszkun

June 1985 **ACM SIGARCH Computer Architecture News , Proceedings of the 12th annual international symposium on Computer architecture ISCA '85**, Volume 13 Issue 3

Publisher: IEEE Computer Society Press, ACM Press

Full text available: pdf(893.17 KB) Additional Information: full citation, citings, index terms

16 A comparison of the object-oriented features of Ada 95 and Java

Benjamin M. Brosgol

November 1997 **Proceedings of the conference on TRI-Ada '97 TRI-Ada '97**

Publisher: ACM Press

Full text available: pdf(2.41 MB) Additional Information: full citation, references, citings, index terms

17 Exception handling in PL/I

M. Donald MacLaren

March 1977 **ACM SIGOPS Operating Systems Review , ACM SIGPLAN Notices , ACM SIGSOFT Software Engineering Notes , Proceedings of an ACM conference on Language design for reliable software**, Volume 11 , 12 , 2 Issue 2 , 3 , 2

Publisher: ACM Press

Full text available: pdf(376.91 KB) Additional Information: full citation, abstract, references, citings, index terms

The PL/I language's facilities for handling exceptional conditions are analyzed. The description is based on the new PL/I standard. Special attention is given to fine points which are not well known. The analysis is generally critical. It emphasizes problems in regards to implementation and structured programming. A few suggestions for future language design are offered.

Keywords: Code generation, Errors, Exceptional conditions, Interrupts, Non-local go-tos, PL/I, Structured programming

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M. Donald MacLaren

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Publisher: ACM Press
Full text available: pdf(3.53 MB) Additional Information: full citation, references, citings, index terms

7 Encryption and Secure Computer Networks

Gerald J. Popek, Charles S. Kline
December 1979 **ACM Computing Surveys (CSUR)**, Volume 11 Issue 4
Publisher: ACM Press
Full text available: pdf(2.50 MB) Additional Information: full citation, references, citings, index terms

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Publisher: ACM Press

Full text available: pdf(1.05 MB) Additional Information: full citation, abstract, references, citings, index terms

This paper presents techniques for simulating directly from a hierarchical circuit description without flattening to the level of primitives. An overview of traditional fault simulation techniques is followed by details of the hierarchical techniques. The fault model is shown to be decoupled from the simulator programs through the use of a fault library. The fault library allows the user to mix both functional and technology-dependent fault models, which allows fault simulation and conseque ...

12 ALGOL: ALGOL bulletin no. 27

F. G. Duncan

September 1968 **ACM SIGPLAN Notices**, Volume 3 Issue 9

Publisher: ACM Press

Full text available: pdf(3.04 MB) Additional Information: full citation

13 Operational characteristics of a harware-based pattern matcher

Roger L. Haskin, Lee A. Hollaar

March 1983 **ACM Transactions on Database Systems (TODS)**, Volume 8 Issue 1

Publisher: ACM Press

Full text available: pdf(1.84 MB) Additional Information: full citation, abstract, references, citings, index terms

The design and operation of a new class of hardware-based pattern matchers, such as would be used in a backended database processor in a full-text or other retrieval system, is presented. This recognizer is based on a unique implementation technique for finite state automata consisting of partitioning the state table among a number of simple digital machines. It avoids the problems generally associated with implementing finite state machines, such as large state table memories, complex cont ...

Keywords: backend processors, computer system architecture, finite state automata, full text retrieval systems, text searching

14 Semantic Road Maps for Literature Searchers

Lauren B. Doyle

October 1961 **Journal of the ACM (JACM)**, Volume 8 Issue 4

Publisher: ACM Press

Full text available: pdf(1.46 MB) Additional Information: full citation, references, citings, index terms

15 Risks to the public

P. G. Neumann
October 1990 **ACM SIGSOFT Software Engineering Notes**, Volume 15 Issue 5
Publisher: ACM Press
Full text available: pdf(1.56 MB) Additional Information: full citation, index terms

16 Reflections on building two Go programs
Bruce Wilcox
October 1985 **ACM SIGART Bulletin**, Issue 94
Publisher: ACM Press
Full text available: pdf(1.42 MB) Additional Information: full citation, abstract, references, citings

From 1972 to 1979 I co-designed and built what became the world's strongest computer Go program, the Reitman-Wilcox Go Program [1]. It took 7 person-years, 8K lines of LISP, 3 megabytes of memory, and an IBM mainframe. Recently I constructed a similar program, called NEMESIS...the Go Master (tm). It has taken 1 person-year, 13.5K lines of C, 146 kilobytes of memory, and an IBM-PC. They play at a similar strength and by similar means. This article discusses both how I went about reengineering the ...

17 A comparison of the object-oriented features of Ada 95 and Java
Benjamin M. Brosgol
November 1997 **Proceedings of the conference on TRI-Ada '97 TRI-Ada '97**
Publisher: ACM Press
Full text available: pdf(2.41 MB) Additional Information: full citation, references, citings, index terms

18 A problem-list of issues concerning computers and public policy
September 1974 **Communications of the ACM**, Volume 17 Issue 9
Publisher: ACM Press
Full text available: pdf(557.29 KB) Additional Information: full citation, citings, index terms

19 Implementation of precise interrupts in pipelined processors
James E. Smith, Andrew R. Pleszkun
June 1985 **ACM SIGARCH Computer Architecture News , Proceedings of the 12th annual international symposium on Computer architecture ISCA '85**, Volume 13 Issue 3
Publisher: IEEE Computer Society Press, ACM Press
Full text available: pdf(893.17 KB) Additional Information: full citation, citings, index terms

20 Assigning wires to layers of a printed circuit board
Frank Rubin
June 1973 **Proceedings of the 10th workshop on Design automation DAC '73**
Publisher: IEEE Press
Full text available: pdf(825.16 KB) Additional Information: full citation, abstract, references, citings, index terms

To minimize vias in a printed circuit board it is valuable to assign each wire to a layer of the board, and to try one-layer paths for each wire before attempting paths with vias. The basis for making the assignment is to minimize a measure of interference between the wires on each layer: straight line intersections, rectangle intersections, and L-path intersections. Of these, the L-path (one horizontal and one vertical line segment)

intersections produce the least congested, most easily ro ...

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